## Intermediate Code Generation ( IR)

IR is an internal form of a program created by the compiler while translating the program from a H.L.L to L.L.L.(assembly or machine code),from IR the back end of compiler generates target code.
Although a source program can be translated directly into the target language, some benefits of using a machine independent IR are:

1. A compiler for different machine can be created by attaching a back end for a new machine into an existing front end.
2. Certain optimization strategies can be more easily performed on IR than on either original program or L.L.L.
3. An IR represents a more attractive form of target code.

## Intermediate Languages:-

1. Syntax Tree and Postfix Notation are tow kinds of intermediate representations, for example $\mathbf{a}=\mathbf{b}^{*}-\mathbf{c}+\mathbf{b}^{*}$ - $\mathbf{c}$


Syntax Tree


DAG

- A $D A G$ give the same information in syntax tree but in compact way because common subexpressions are identified.
- Postfix notation is a linearized representation of a syntax tree, for example: abc-*bc-*+=
- Two representation of above syntax tree are:


2. Three-Address Code is a sequence of statements of the general form :

$$
X=Y o p Z \quad / / \text { op is binary arithmetic }
$$ operation

For example : $x+y * z$

$$
\begin{aligned}
& t 1=y * z \\
& t 2=x+t 1
\end{aligned}
$$


where $\mathrm{t} 1, \mathrm{t} 2$ are compiler generated temporary.

## Types of three address code statement:-

```
1. Assignment statements of the form \(X=Y o p Z\) ( where op is a binary arithmetic or logical operator).
2. Assignment instructions of the form \(X=o p Y\) ( op is a unary operator).
3. Copy statements of the form \(X=Y .4\).
Unconditional jump ( Goto L ).
5. Conditional jump (if \(X\) relop Ygoto \(L\) ).
6. Param \(X\) \& Call \(P, N\) for procedure call and and return \(Y\), for example :
Param x1x2
Param
........ \(\quad\) xn
Param Pn
Call
```

7. Index assignments of the form $\mathrm{X}=\mathrm{Y}[\mathrm{i}] \& \mathrm{X}[\mathrm{i}]=\mathrm{Y}$.
8. Address \& Pointer Assignments

$$
\begin{aligned}
X & =\& Y \\
X & =* Y \\
* X & =Y
\end{aligned}
$$

Example : $\mathrm{a}=\mathrm{b}^{*}-\mathrm{c}+\mathrm{b}^{*}-\mathrm{c}$

$$
\begin{aligned}
& t 1=-c \\
& t 2=b * t 1 \\
& t 3=-c \\
& t 4=b * t 3 \\
& t 5=t 2+t 4 \\
& a=t 5
\end{aligned} \quad \begin{aligned}
\text { Three address code } \\
\text { For syntax tree }
\end{aligned}
$$

$$
\begin{aligned}
& t 1=-c \\
& t 2=b * t 1 t 5 \\
& =t 2+t 2 \\
& a=t 5
\end{aligned}
$$

Note: Three-address statements are a kin to assembly code statements can have symbolic labels and there are statements for flow of control.

## Implementation of Three Address Code :-

In compiler, three-address code can be implement as records, with fields for operator and operands.

1. Quadruples :- It is a record structure with four fields:

- OP // operator
- $\quad \arg 1, \arg 2 / /$ operands
- result

2. Triples :- To avoid entering temporary into $S T$, we might refer to a temporary value by position of the statement that compute it. So three address can be represent by record with only three fields:

- OP // operator
- $\quad \arg 1, \arg 2 / /$ operands

Example: $\mathbf{a}=\mathbf{b}$ * $-\mathbf{c}+\mathbf{b}$ *- $\mathbf{c}$
i. By Quadruples

| Position | OP | arg1 | arg2 | result |
| :---: | :---: | :---: | :---: | :---: |
| 0 | - | c |  | $\mathbf{t}$ |
| 1 | $*$ | b | $\mathbf{t} 1$ | $\mathbf{t}$ |
| 2 | - | c |  | $\mathbf{t 3}$ |
| 3 | $*$ | b | $\mathbf{t 3}$ | $t 4$ |
| 4 | + | $\mathbf{t}$ | $\mathbf{t} 4$ | $\mathbf{t 5}$ |
| 5 | $=$ | $\mathbf{t}$ |  | a |

ii. By Triples

| Position | OP | arg1 | arg2 |
| :---: | :---: | :---: | :---: |
| $\mathbf{0}$ | - | c |  |
| 1 | $*$ | b | $(0)$ |
| 2 | -3 | c |  |
|  | $*$ | b | $(2)$ |
| 4 |  | $(1)$ | $(3)$ |
|  | + | $\mathbf{a}$ | $(4)$ |
| 5 |  |  |  |

