Ministry of Higher Education and Scientific Research UNIVERSITY OF ANBAR COLLEGE of COMPUTER SCIENCES AND INFORMATION TECHNOLOGY DEPT. COMPUTER NETWORKS SYSTEMS



وزارة التعليم العالي والبحث العلمي جامسعية الانبار كليية علوم الحاسوب وتكنولوجيا المعلومات قسم أنظمة شبكات الحاسوب

MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

	Module Information معلومات المادة الدراسية						
Module Title Logic 1				Modi	ule Delivery		
Module Type		Core	Core		☑ Theory		
Module Code		NSCC109			□ Lecture ☑ Lab		
ECTS Credits		5			☐ Tutorial		
SWL (hr/sem)		125			☐ Practical☐ Seminar		
Module Level		First Class	Semester of Delivery				
Administering Dep	partment	NSD	College	CSIT			
Module Leader			e-mail				
Module Leader's Acad. Title			Module Lea	der's Qı	ualification		
Module Tutor			e-mail				
Peer Reviewer Name			e-mail				
Scientific Committee Approval Date			Version Nu	mber			

Relation with other Modules					
	العلاقة مع المواد الدراسية الأخرى				
Prerequisite module	None	Semester			
Co-requisites module	None	Semester			

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Module Aims, Learning Outcomes and Indicative Contents				
	أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية			
Module Aims أهداف المادة الدراسية	The module aims to provide students with a solid understanding of digital logic principles and concepts. Students learn about Boolean algebra, logic gates, truth tables, and digital logic circuits.			
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	Demonstrate a solid understanding of digital logic principles, including Boolean algebra, logic gates, truth tables, and the concept of binary representation.			
Indicative Contents المحتويات الإرشادية	Introduction to Digital Logic Combinational Logic Design Arithmetic circuits Sequential Logic Design Circuit Testing and Verification			

Learning and Teaching Strategies					
	استراتيجيات التعلم والتعليم				
Conceptual Understanding					
	Problem-Solving Approach				
	Hands-on Laboratory Experience				
Strategies	Design Projects				
	Simulation and Modeling				
	Problem-Based Learning				

Student Workload (SWL)			
الحمل الدر اسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	63	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4.2

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Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	62	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	4.1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

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		Time/Nu	Maight (Marks)	Week Due	Relevant Learning	
		mber	Weight (Marks)		Outcome	
	Quizzes	2	10% (10)	5,10	LO #1,2, 3 and 5	
Formative	Assignments	2	10% (10)	2,12	LO # 3, 4 and 5	
assessment	Projects / Lab.	1	10% (10)	Continuous		
	Report	1	10% (10)	13	LO # 5,8 and I0	
Summative	Midterm Exam	2 hr	10% (10)	7	LO # 1-6	
assessment	Final Exam	3 hr	50% (50)	16	All	
Total assessment		100% (100 Marks)				

	Delivery Plan (Weekly Syllabus)				
	المنهاج الاسبوعي النظري				
	Material Covered				
Week 1	Introduction: Digital System				
Week 2	Number Systems				
Week 3	Octal and Hexadecimal Numbers				
Week 4	Number base conversion				
Week 5	Theories of Boolean Algebra				
Week 6	Digital Logic gates				
Week 7	Boolean Expression and Truth table				
Week 8	Sum Of Product Simplification				
Week 9	Product Of Sum Simplification				
Week 10	k 10 Exclusive OR				
Week 11	NAND gates				
Week 12	NOR gates				
Week 13	Two- and Three-Variables Karnaugh Maps.				

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Week 14	Four Variables Karnaugh Maps.		
Week 15	Quine-McCluskey method		
Week 16	Final Exam		

Delivery Plan (Weekly Lab. Syllabus)				
المنهاج الاسبوعي للمختبر				
	Material Covered			
Week 1	Introduction to logic gates: AND, OR, NOT			
Week 2	Constructing truth tables for basic logic operations			
Week 3	Designing and building simple logic circuits using logic gates			
Week 4	Verifying the functionality of logic circuits through experimentation			
Week 5	Boolean algebra and simplification technique			
Week 6	Applying Boolean algebra to simplify logic circuits			
Week 7	Advanced logic gates: XOR, NAND, NOR			

Learning and Teaching Resources مصادر التعلم والتدريس				
Text Libra				
Required Texts	Digital fundamentals, Thomas L. Floyd, 11 th edition Digital Design, Morris Mano, 4 th edition			
Recommended Texts	An Introduction to Logic Technology Fundamentals of logic design			
Websites				

Grading Scheme						
مخطط الدرجات						
Group Grade التقدير Marks (%) Definition						

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Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	ختر	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
(0 – 49)	F – Fail	راسب	(0-44)	Considerable amount of work required

Note: Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.