

# MODULE DESCRIPTION FORM

## نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	<b>Microprocessors</b>		Module Delivery
Module Type	C		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	<b>CSDC120</b>		
ECTS Credits	6		
SWL (hr/sem)	<b>150</b>		
Module Level	UGI	Semester of Delivery	
Administering Department	CSIT	College	Type College Code
Module Leader	Name	e-mail	E-mail
Module Leader's Acad. Title	Professor	Module Leader's Qualification	Ph.D.
Module Tutor	Name (if available)	e-mail	E-mail
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	01/06/2023	Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

### أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<b>Module Objectives</b> أهداف المادة الدراسية	1. The student will be able to understand and understand the mechanics of their algorithmic data repair problems in terms of their degree of complexity. 2. Trees, how to build them in C++, self-recall, and how to deal with them 3.. that the student be able to understand the working mechanics of algorithms for data structures 4.sorting algorithm
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	This article is based on knowledge Learn to program in C++ in a professional way
<b>Indicative Contents</b> المحتويات الإرشادية	

## Learning and Teaching Strategies

### استراتيجيات التعلم والتعليم

<b>Strategies</b>	Understand code and algorithms and implement them in different ways and new steps
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## Student Workload (SWL)

### الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا

<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	93	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	6
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	57	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	4
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	<b>100</b>		

## Module Evaluation

### تقييم المادة الدراسية

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5 and 10	LO #1, #2 and #10, #11
	Assignments	2	10% (10)	2 and 12	LO #3, #4 and #6, #7
	Projects / Lab.	1	5% (5)	Continuous	All

	<b>Report</b>	1	5% (5)	13	LO #5, #8 and #10
<b>Summative assessment</b>	<b>Midterm Exam</b>	2hr	20% (20)	7	LO #1 - #7
	<b>Final Exam</b>	3hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b> المنهاج الاسبوعي النظري	
	<b>Material Covered</b>
<b>Week 1</b>	Introduction to micro processor
<b>Week 2</b>	Evolution from 8080/8085 to 8086
<b>Week 3</b>	Pipelining, Registers
<b>Week 4</b>	ADD instruction:, mov instruction:
<b>Week 5</b>	INTRODUCTION TO PROGRAM SEGMENTS
<b>Week 6</b>	Data segment,
<b>Week 7</b>	Mid-term Exam
<b>Week 8</b>	Extra segment (ES) , Memory map of the IBM PC, What is a stack
<b>Week 9</b>	A few more words about segments in the 80x86 , Overlapping, Flag register
<b>Week 10</b>	Flag register con., Flag register and ADD instruction Use of the zero flag for looping
<b>Week 11</b>	Use of the zero flag for looping con., 80x86 Addressing Modes A,B,C,D
<b>Week 12</b>	80x86 Addressing Modes E,F,G, Segment overrides
<b>Week 13</b>	CONTROL TRANSFER INSTRUCTIONS, FAR and NEAR
<b>Week 14</b>	Unconditional jumps, statements
<b>Week 15</b>	CALL & Assembly language subroutines
<b>Week 16</b>	<b>Preparatory week before the final Exam</b>

<b>Delivery Plan (Weekly Lab. Syllabus)</b> المنهاج الاسبوعي للمختبر	
	<b>Material Covered</b>
<b>Week 1</b>	Eum8086-1
<b>Week 2</b>	Eum8086-2

<b>Week 3</b>	MOV + ADD instruction
<b>Week 4</b>	SUB instruction
<b>Week 5</b>	Push +POP instruction
<b>Week 6</b>	Flag register ,jump
<b>Week 7</b>	Flag register
<b>Week 8</b>	<b>Arduino uno board</b>
<b>Week 9</b>	<b>Arduino uno PORT</b>
<b>Week 10</b>	<b>Arduino C Language &amp; Instruction</b>
<b>Week 11</b>	led Blinking
<b>Week 12</b>	Led Blinking& PUSH button
<b>Week 13</b>	Potentiometer
<b>Week 14</b>	Photo resistor as light sensor

### Learning and Teaching Resources

#### مصادر التعلم والتدريس

	Text	Available in the Library?
<b>Required Texts</b>	Introduction to 8086 Assembly Language Programming , Joe Carthy, UCD	Yes
<b>Recommended Texts</b>		
<b>Websites</b>		

### Grading Scheme

#### مخطط الدرجات

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

