	Ministry of Higher Education and Scientific Research. University of Anbar. Department of Information System.	
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## MODULE DESCRIPTOR FORM

Module Information						
Module Title	Logic Design I		Mod	lule Type	Түре с	
Module Code	e ISLD103 ECTS Credit		lits	ts 6		
Module Level		UGI	Semester	mester of Delivery One		One
Administering D	epartment	IS	Faculty	CSIT		
Module Leader		odulwahed Salman e-mail Co		Co.moi	Co.montasser.salman@uoanbar.e	
Ploudic Ecuaci	Abdulaziz		C man	du.iq	q	
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification		PhD.	
Module Tutor		e-mail				
Peer Reviewer Name /		/	e-mail	/		
Review Committee Approval		DD/MM/YY	Version N	umber	2.0	

Relation With Other Modules			
Pre-requisites	/		
Co-requisites	/		
Modu	Module Aims, Learning Outcomes and Indicative Contents		
Module Aims	<ul> <li>-The student should understand number systems and codes and the conversion between them.</li> <li>-The student should understand the Boolean expression and how to apply it.</li> <li>-The student should recognize among different logic gates and how to use them.</li> <li>-The student should understand how to design a logic circuit.</li> <li>-The student should understand using K-map for simplification.</li> </ul>		
Module Learning	A-Knowledge and Understanding		

Outcomes	A1. The student should understand number systems and codes and the conversion			
	between them.			
	A2. The student should understand the Boolean expression and how to apply it.			
	A3. The student should recognize among different logic gates and how to use them.			
	A4. The student should understand how to design a logic circuit.			
	A5. The student should understand using K-map for simplification			
Indicative Contents				
	Learning and Teaching Strategies			
Strategies	The main strategy that will be adopted in delivering this module are: 1. Power point presentation (Data show). 2. Explanation on the white board using different color markers. 3. Discussions with the student during teaching. 4. Interaction with students through daily problems practice through lecture. 5. Solve different problems with more exercises. 6. Submit assignment that develop student learning.			

Module Delivery		
Structured workload (h/w)	6.4	
Unstructured workload (h/w)	3.6	
Total workload (h/w)	10	

Module Evaluation					
	Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome	
Quizzes	٣	6% (6)	3,7 and 11		
Assignments	2	6% (6)	2 and 12		
Projects / Lab.	1	1°% (1°)	Continuous		
Report	1	5% (5)	13		
Midterm Exam	2 hr	18% (18)	7		
Final Exam	3 hr	50% (50)	16		
Total		100% (100 Marks)			

Learning and Teaching Resources				
	Text	Available in the Library?		
Required Texts		Yes/No		
Recommended Texts		Yes/No		
Websites				

	Delivery Plan (Weekly Syllabus)		
	Material Covered		
Week 1	Introduction to number system		
Week 2	Conversion between systems		
Week 3	Codes and conversion between them		
Week 4	Boolean expression		
Week 5	Logic gates		
Week 6	Logic gates design		
Week 7	Mid-Term Exam		
Week 8	NAND gates		
Week 9	NOR gates		
Week 10	Sum of product form		
Week 11	Product Of sum form		
Week 12	Product Of sum form		
Week 13	K-map		
Week 14	K-map		

Week 15	Preparatory Week
Week 16	Final Exam

## **APPENDIX:**

UNIVERSITY of Anbar				
GRADING SCHEME				
Group	ECTS Grade	% of Students/Marks	Definition	GPA
	A - Excellent	Best 10%	Outstanding Performance	5
g	<b>B</b> - Very Good	Next 25%	Above average with some errors	4
Success Group (50 - 100)	C - Good	Next 30%	Sound work with notable errors	3
(50 - 100)	<b>D</b> - Satisfactory	Next 25%	Fair but with major shortcomings	2
	E - Sufficient	Next 10%	Work meets minimum criteria	1
Fail Group (0 – 49)	FX – Fail	(45-49)	More work required but credit awarded	
	F – Fail	(0-44)	Considerable amount of work required	
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The university has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.