



#### 4- Nodal Analysis

In per-phase analysis the components of the power transmission system are modeled and represented by passive impedances or equivalent admittances accompanied, where appropriate, by active voltage or current sources. In the steady state, for example, a generator can be represented by the circuit of either Fig.1 (a) or Fig.1 (b). The circuit having the constant emf  $E_s$  ' series impedance  $Z_a$  ' and terminal voltage  $V$  has the voltage equation

$$E_s = IZ_a + V$$

Dividing across by  $Z_a$  gives the current equation for Fig. 1 (b)

$$I_s = \frac{E_s}{Z_a} = I + VY_a$$

where  $Y_a = 1 /Z_a$  ' Thus, the emf  $E_s$  and its series impedance  $Z_a$  can be interchanged with the current source  $I_s$  and its shunt admittance  $Y_a$  , provided

$$I_s = \frac{E_s}{Z_a} \quad \text{and} \quad Y_a = \frac{1}{Z_a}$$

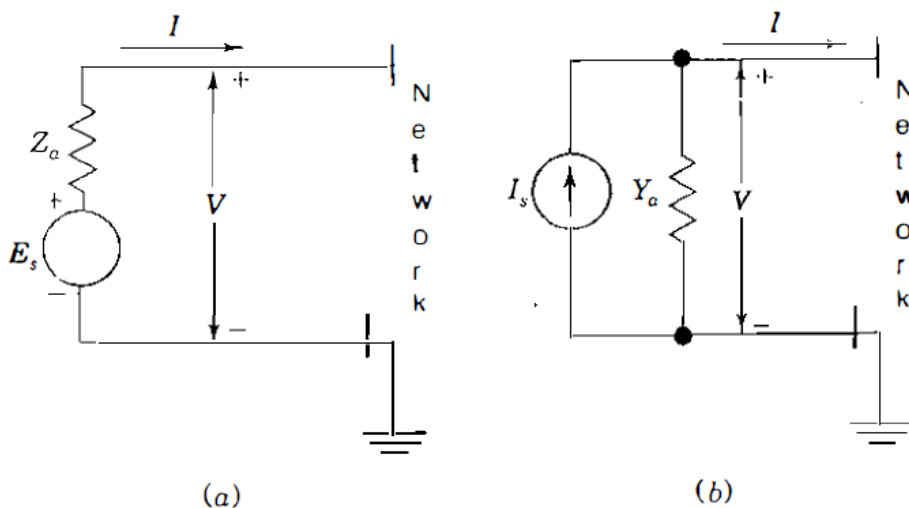
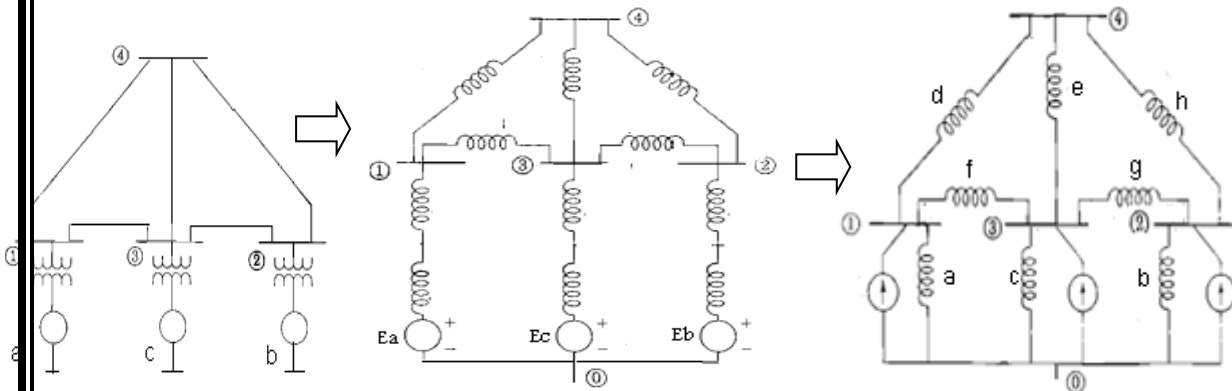


Figure 1 Circuits illustrating the equivalenc of sources when  $I_s = E_s/Z_a$  and  $Y_a = 1 /Z_a$

Systematic formulation of equations determined at nodes of a circuit by applying Kirchoff current law. In electrical power networks, the reference node is the ground (major node), then replace the e.m.f,s and series impedance by equivalent current source and shunt admittances.



For node (1):

$$I_1 = V_1 Y_a + (V_1 - V_3) Y_f + (V_1 - V_4) Y_d$$

$$I_1 = V_1 (Y_a + Y_f + Y_d) - V_3 Y_f - V_4 Y_d \quad \dots\dots\dots 1$$

$$0 = (V_4 - V_1) Y_d + (V_4 - V_2) Y_h + (V_4 - V_3) Y_e$$

$$0 = -V_1 Y_d - V_2 Y_h - V_3 Y_e + V_4 (Y_d + Y_e + Y_h) \quad \dots\dots\dots 2$$

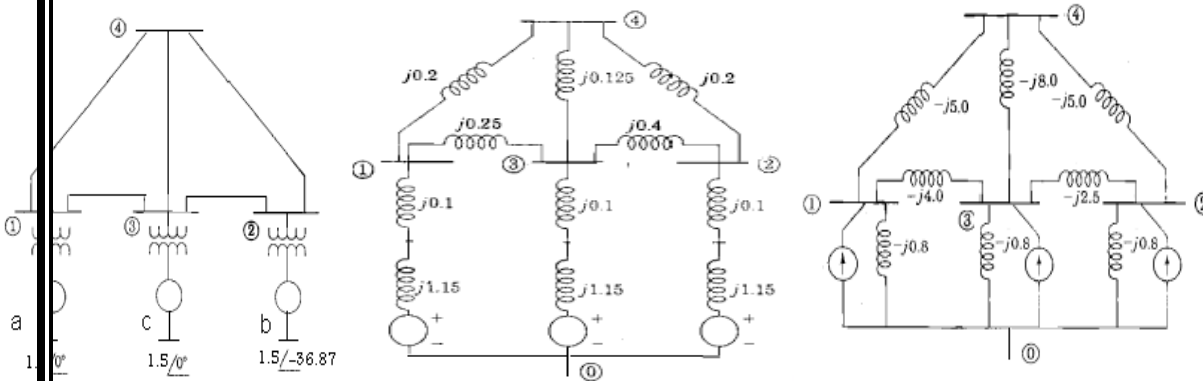
$$\begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} & Y_{13} & Y_{14} \\ Y_{21} & Y_{22} & Y_{23} & Y_{24} \\ Y_{31} & Y_{32} & Y_{33} & Y_{34} \\ Y_{41} & Y_{42} & Y_{43} & Y_{44} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix}$$

The (Y) matrix is designated ( $Y_{bus}$ ) and called (the bus admittance matrix)

- 1- Each diagonal element in the nodal admittance matrix ( $Y_{11} Y_{22} Y_{33} Y_{44}$ ) is called self admittance and it is the sum of the admittances of the branches terminating in each node.
- 2- Each off-diagonal element of the nodal admittance matrix (all the other admittances) called the mutual admittances and each equal the negative of the sum of all admittances connected between the nodes identified by the double subscript.
- 3- If no direct connection exists between any two nodes the corresponding off- diagonal elements will be zero.

Example:

Write in matrix form the node equations necessary to solve for the voltages of the numbered buses of the figure below.



Solution:

$$I_1 = I_3 = \frac{1.5 \angle 0}{j1.25} = 1.2 \angle -90 = -j1.2 \text{ p.u}$$

$$I_2 = \frac{1.5 \angle -36.87}{j1.25} = 1.2 \angle -126.87 = -0.72 - j0.96 \text{ p.u}$$

Self admittances

$$Y_{11} = -j5 - j4 - j0.8 = -j9.8$$

$$Y_{22} = -j5 - j2.5 - j0.8 = -j8.3$$

$$Y_{33} = -j4 - j2.5 - j8 - j0.8 = -j15.3$$

$$Y_{44} = -j5 - j5 - j8 = -j18$$

Mutual admittances

$$Y_{12} = Y_{21} = 0, \quad Y_{23} = Y_{32} = j2.5, \quad Y_{13} = Y_{31} = j4$$

$$Y_{14} = Y_{41} = j5, \quad Y_{24} = Y_{42} = j5, \quad Y_{34} = Y_{43} = j8$$

The node equations in matrix form

$$\begin{array}{c|c|c|c|c} -j1.2 & & -j9.8 & j0 & j4 & V_1 \\ -0.72- & = & j5 & & & V_2 \\ j0.96 & & j0 & -j8.3 & j2.5 & V_3 \\ -j1.2 & & j5 & & & V_4 \\ 0 & & j4 & j2.5 & -j15.3 & \end{array}$$



		$j5$	$j5$	$j8$		
		$-j18$				

