

Fundumental of Electronic I Msc: Munther Naif Thiyab

# Fundumantal of Electronic I

#### Second Class

#### Chapter01: Semiconductor Diodes Lec01\_p3 Munther N. Thiyab

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# **Zener Region**

The Zener region is in the diode's reverse-bias region.

At some point the reverse bias voltage is so large the diode breaks down and the reverse current increases dramatically.

- The maximum reverse voltage that won't take a diode into the zener region is called the peak inverse voltage (PIV) or peak reverse voltage (PRV).
- The voltage that causes a diode to enter the zener region of operation is called the zener voltage (V<sub>Z</sub>).





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# **Forward Bias Voltage**

The point at which the diode changes from no-bias condition to forward-bias condition occurs when the electrons and holes are given sufficient energy to cross the p-n junction. This energy comes from the external voltage applied across the diode.

The forward bias voltage required for a:

- gallium arsenide diode  $\cong$  1.2 V
- silicon diode  $\cong 0.7 \text{ V}$
- germanium diode  $\cong 0.3$  V



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## **Comparison Ge, Si, GaAs**





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# **Temperature Effects**

As temperature increases it adds energy to the diode.

- It reduces the required forward bias voltage for forward-bias conduction.
- It increases the amount of reverse current in the reverse-bias condition.

Germanium diodes are more sensitive to temperature variations than silicon or gallium arsenide diodes.



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## **Temperature Effects**





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## **Resistance Levels**

Semiconductors react differently to DC and AC currents.

There are three types of resistance:

- DC (static) resistance
- AC (dynamic) resistance
- Average AC resistance



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## **AC (Dynamic) Resistance**





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AC (Dynamic) Resistance  
since 
$$I_D = I_s \left( e^{V_D / nV_T} - 1 \right)$$
,  $\frac{dI_D}{dV_D} = \frac{I_s}{nV_T} e^{V_D / nV_T}$   
 $\frac{dI_D}{dV_D} = \frac{1}{nV_T} \left( I_D + I_s \right)$ , since  $I_D >> I_s$ ,  $\frac{dI_D}{dV_D} \cong \frac{I_D}{nV_T}$   
 $\frac{dV_D}{dI_D} = r_D = \frac{nV_T}{I_D}$   
for n=1, and at room temperature of 27°C, T=273+27=300K  
 $V_T = \frac{KT}{q} = \frac{\left( 1.38 \times 10^{-23} \right)}{1.6 \times 10^{-19}} \cong 26mV$   
 $r_D = \frac{26mV}{I_D}$ 

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# **AC (Dynamic) Resistance**

In the forward bias region:  $r'_d = \frac{26 \,\mathrm{mV}}{I_D} + r_B$ 

- The resistance depends on the amount of current  $(I_D)$  in the diode.
- $r_D = 26 \text{ mV/I}_D$  is the resistance of the p-n junction and does not include the resistance of the semiconductor material itself (the body resistance).
- $r_R$  is added to account for body resistance and it ranges from a typical 0.1  $\Omega$  to 2  $\Omega$ .

In the reverse bias region:

$$\mathbf{r}_{\mathbf{d}}' = \infty$$

The resistance is effectively infinite. The diode acts like an open.



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